



PATENT  
Atty. Docket No.: SONY-14500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FILED  
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BY

In re Application of )

Bruce A. Fairman )

Serial No. 09/708,114 )

Filed: November 6, 2000 )

For: **PROGRAMMABLE FIRST-IN  
FIRST-OUT (FIFO) MEMORY  
BUFFER FOR CONCURRENT  
DATA STREAM HANDLING** )

Group Art Unit: 2182

Examiner: Huynh, Kim Ngoc

**RESPONSE TO OFFICE ACTION  
MAILED ON November 13, 2003**

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Commissioner for Patents  
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Technology Center 2100

Sir:

REMARKS

2/5/04  
Applicant respectfully requests further examination and reconsideration in view of the arguments set forth fully below. Claims 1-46 were previously pending in this Application. Within the Office Action, Claims 1-46 have been rejected. Claims 1-46 are now pending in the application.

Rejections Under 35 U.S.C. § 102

Within the Office Action, Claims 1-4, 6, 7, 9-12, 14, 15, 17-20, 22, 23, 25-28, 30, 31, 33-35, 37, 38, 40-42, 44 and 45 have been rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,516,371 to Lai et al. (hereinafter "Lai"). The applicant respectfully disagrees with this rejection.

Lai teaches a network interface device for buffering data wherein the device includes a random access memory, a read offset register configured to store read pointer information, and a read controller configured to read data stored in the random access memory based on the read